METHOD FOR FORMING A JUNCTION REGION OF A SEMICONDUCTOR DEVICE

ABSTRACT OF THE INVENTION

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method for forming a junction region Α o f semiconductor device is disclosed. The steps of the method include providing a semiconductor substrate. gate structure is formed on the semiconductor substrate. A dopant is implanted into the semiconductor substrate to form the junction region. An insulator layer is formed on the gate structure and the semiconductor substrate. carbon-containing plasma treatment is performed to the insulator layer. A spacer is formed on a side-wall of the gate structure and the dopant is implanted into the semiconductor substrate to form a source/drain region next to the junction region. A heat treatment is performed to the semiconductor substrate.

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